SOFT SWITCHING OF INTERLEAVED BOOST CONVERTER FED PMBLDC MOTOR USING PI AND FUZZY CONTROLLERS

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Abstract:
This paper proposes an improved interleaved boost converter topology for soft switching of VSI fed PMBLDC motor. Six switch and four switch converter configurations are employed for the proposed boost converter under zero voltage switching and zero current switching. The proposed boost converter for PMBLDC motor under various operating conditions are implemented in MATLAB/SIMULINK environment. The feasibility of six switch and four switch converter by for PMBLDC motor is explored. A comparative evaluation of speed control of six switch and four switch inverter with fed PMBLDC motor. PI and FUZZY controllers has been made discussed in this paper.

Key words: Four switch VSI, Fuzzy logical control, PI, PMBLDC Motor, Six switch VSI.

1. Introduction

High frequency switching in converter results in reducing losses, the traditional boost converter will employ zero voltage and zero current switching. In the past numerous soft switching methods are developed for boost converters [1]-[2]. The zero voltage switching (ZVS) and zero current switching (ZCS), strategy the losses due to switching are reduced in the main switch. Therefore, the efficiency of ZVS or ZCS based topology is improved compared to conventional topology [3]-[4].

In the past, numerous soft switching methods are developed for boost converters. The losses due to soft switching are reduced in case of zero voltage switching (ZVS) and zero current switching (ZCS) topologies and efficiency is improved compared to the conventional converter [4]-[5]. PMBLDC motor many advantages when compared with other type of AC motors [6]-[7]. Torque ripple in PMBLDC motor may be related to inverter or motor design factors of the motor, which results non ideal current waveforms, this causes speed oscillations and wear and tear of mechanical portions of the drive, which results in vibration and noise of the motor[8]. The soft switching techniques reduce the switching voltage stress of the main switches of the converter. This topology has some advantages which include low cost, reduction in switching losses, improved efficiency [9]-[10]. Speed control of a drive refers to maintaining constant speed, with any variation of electric power [11]-[12]. The purpose of speed control is to maintain constant speed and to make it less sensitive external disturbances [13]. There are many method of speed Control, among them PI controller is most common and FUZZY controller most flexible controller[14]-[15]. PI is used in numerous industrial applications while fuzzy control robust and possess noise rejection capabilities.

In this paper, comparison has been made between PI and Fuzzy controller interleaved boost converter based VSI fed PMBLDC motor drive. Here, four switch and six switch converter continuous are considered for VSI and proposed work is carried out for different operating conditions and simulation results are presented to validate the proposed work.

2. Proposed Control Schemes of Interleaved boost converter with ZVS and ZCS fed PMBLDCM Drive

2.1. Interleaved boost converter with ZVS and ZCS for Six switch three phase VSI fed PMBLDC Motor

Fig.1 shows the block diagram of Interleaved Boost Converter with ZCS and ZVS fed BLDC motor. There are two control loops; one is the speed control loop which is outer loop and another inner current loop. The speed error obtained by comparing the actual speed with the desired reference speed. The speed error is fed to the PI and Fuzzy logical controller to obtain the reference torque and compared with actual torque of PMBLDC Motor.
The resultant torque error is multiplied with suitable constant and is amplified to provide input to reference current block. The reference current is compared with phase currents of PMBLDC Motor which is fed to hysteresis current controller. The hysteresis current controller generates pulses for operation of three leg inverter of PMBLDC Motor drive. A rate limiter is introduced, which limits the current within specified limits.

2.2. Interleaved Boost Converter with ZVS and ZCS for Four Switch Three Phase VSI fed PMBLDC Motor

Fig.2 shows the closed loop speed control of four switch three Phase Inverter fed PMBLDCM drive with interleaved boost converter. Fig.2 shows the three phase inverter four switch fed BLDC motor with interleaved Boost Converter. With closed loop mode of speed control. The motor of speed control is advantageous because of low cost (2 -capacitor and only 4 - switches are utilised) and Lower losses during switching operation. The faster dynamic response of drive reduces ripples in torque, lower voltage stress and increase in the overall performance of the system.

2.3. Interleaved Boost Converter

Fig3. The circuit consists of inductor Lr, capacitor Cr, which are the resonance elements along with Csa,Csb which represent the parasitic capacitances. In order to achieve ZVS, ZCS characteristics, the current consists of Sr, auxiliary switch to aid the phenomenon of resonance. The components of interleaved boost converter are described in.

High voltage gain DC boost converter that proposed in this paper is a combination of two 2 phase interleaved boost converters and is shown by Fig.2. The four power electronic based switching devices used in the Fig.2 are controlled in 90°to each other’s simultaneously (interleave technique method), in order to smooth output ripple current, raising power rating and efficiency. For the Fig.3, from KVL, voltage equation is given by equation (1)

\[ V_o = V_{sa} + V_{sb} - V_{in} \]  

(1)

Where, \( V_s \) = Supply voltage ,  
\( V_o \) = Output voltage,  
\( V_{sa} \) = Voltage across Capacitor “C_a”,  
\( V_{sb} \) = Voltage across Capacitor “C_b”

Voltage gain (G) for the fig.2 is given by equation (2)
\[ G = \frac{V_o}{V_i} = \frac{1 + D}{1 - D} \quad (2) \]

Where, \( D \) = Duty cycle.

In the proposed high voltage gain interleaved DC boost converter, there is a considerable reduction in input current ripple and inductor size. The output voltage ripple of the circuit depends on the size of capacitor. The proposed converter should be operated in Continuous Conduction Mode (CCM). Based on the equations (3) & (4), the inductance & capacitance values respectively can be decided for the proposed converter.

\[ L = \frac{DV_i}{4f_s} \Delta I_L \quad (3) \]
\[ C_{bus} = \frac{DI_{out}}{2 \Delta V_{bus} f_s} \quad (4) \]

Where, \( \Delta I_L \) = Maximum current ripple
\( \Delta V_{bus} \) = Output voltage ripple
\( I_{out} \) = Output current
\( f_s \) = Switching frequency for the proposed converter.

Here the study of the Interleaved boost converter PMBLDC Motor is operated in the PI and FUZZY.

### 2.4. Fuzzy Logical Controller

The conventional PI controllers are fixed-gain controllers i.e., the proportional and integral gains are constant. So, this type of controllers will not compensate properly, if the parameter changes and it does not adjust with changes in environment. The response of the PI-controller is too slow due to its sluggish response to relatively prompt variations in the state which will result more settling time. In addition to finding the gain constants related to system is very difficult. Therefore the fuzzy control algorithm is capable of improving the system performance as compared with the classical methods. The two FLC input variables are the electromagnetic torque and change of electromagnetic torque. The operation of a fuzzy logic controller depends on the shape of membership functions for rule base.

In this paper a fuzzy logic control scheme (Fig 1 & Fig 2) is proposed for speed control of a PMBLDC motor. The fuzzy logic controller has advantages to be robust and relatively simple to design. The irrespective of system exact model we can able to design fuzzy logic controller. The fuzzy rule based approach is done through three stages. They are Fuzzification, inference engine and defuzzification.

### 2.5. Fuzzification

Fuzzy logical controller is an advanced controlled using multi valued logical. It is works on the principle of rule based system. It involves two processes namely fuzzification and defuzzification. Fuzzification involves the process of transforming a constant value into linguistic variable while defuzzification deals with hienstic variable conversion to constant value. In other words defuzzification is the inverse process of fuzzification of each input in graphical form. bell shaped is most common used membership function. Fuzzy logic is employed in speed Control with error in speed (error), the rate of change of speed (cerror)

Fig 4: shows the basic block diagram of fuzzy logic controller (FLC)

Fig 5: simulink diagram implementation of Fuzzy logic controller (FLC). The two inputs namely error and cerror fed FLC. The input is also change in speed and derivative terms is added to get rate of change of speed. The rule based system is incorporated is simu link block of fuzzy logic controller. The member ship function considered here triangular shape. The following are various linguistic terms for the FLC
- Negative Big (NB)
- Negative Medium (NS)
- Zero (Z)
- Positive Medium (PM)
- Positive Big (PB)
After assigning the input, output ranges to define fuzzy sets, mapping each of the possible seven input fuzzy values of speed deviation, active power deviation to the seven output fuzzy values is done through a rule base. Thus the Fuzzy. Associative Memory (FAM) comes into picture (Fig6, Fig7, Fig 8).

Fig6: Membership functions for error and change in ‘error’

Fig7: Membership functions for error and change in ‘cerror’

Fig8: Shows the membership function of output variable

Table -1

<table>
<thead>
<tr>
<th>Error/error</th>
<th>NB</th>
<th>NM</th>
<th>ZO</th>
<th>PM</th>
<th>PB</th>
</tr>
</thead>
<tbody>
<tr>
<td>NB</td>
<td>NB</td>
<td>NB</td>
<td>NB</td>
<td>NM</td>
<td>Z</td>
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<tr>
<td>PB</td>
<td>Z</td>
<td>PM</td>
<td>PB</td>
<td>PB</td>
<td>PB</td>
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Rules base:
1. If error in NB and error is NB, output is NB
2. If error is NB and c error is NM, output is NB
3. If error is NB and cerror is Z ,output is NB
4. If error is NB and cerror is PM ,output is NM
5. If error is NB and cerror is PB,output is Z

Similarly the remaining other rules are written in similar fashion. which are tabulated in Table-1.

2.6. Design of PMBLDC Motor

A PMBLDC Motor has three stator phase windings connected in a star manner. Fig 9: shows the equivalent circuit of a PM BLDC Motor.

The windings of a BLDC Motor modelled as a series combination of RL and speed depends on the voltage source, which is known as the back EMF .The BLDCM has three phases and those phase
voltages are given by the equations

\[ V_{ao} = R_i + L \frac{di_a}{dt} + e_a + V_{no} \] (5)

\[ V_{bo} = R_i + L \frac{di_b}{dt} + e_b + V_{no} \] (6)

\[ V_{co} = R_i + L \frac{di_c}{dt} + e_c + V_{no} \] (7)

Fig 10: PMBLDC Motor Waveforms of ideal back EMF and phase current

Equation (5) & (6) & (7) Where, \( V_A \) Voltage of phase ‘A’, \( V_B \) Voltage of phase ‘B’ \( V_c \) Voltage of phase ‘C’ \( I_A \) Current of phase ‘A’, \( I_B \) Current of phase ‘B’, \( I_c \) Current of phase ‘C’ \( R_i \) Stator resistance \( e_a \) Phase “A” stator flux linkages \( e_b \) Phase “B” stator flux linkages \( e_c \) Phase “C” stator flux linkages \( e_a \) Phase “A” back EMF \( e_b \) Phase “B” back EMF \( e_c \) Phase “C” back EMF.

Fig 10: Shows the relationship between the back EMF waveform of an ideal PMBLDC Motor and the armature current. The shape of the currents should in rectangular waveform and must be in phase with the corresponding phase back EMF in Table-2.

If the self and mutual inductance around the air gap are consider to be constant, then there will be a direct relation between the applied source voltage to the phase terminals (V) and the induced back EMF (E) is given by equation(10) and the electromagnetic torque \( (T_e) \) in N.M is given by equation (11).

\[ W_r(n) = W_r(n) + W_r(n) \] (11)

The new value of torque reference is given by

\[ T(n) = T_{(n-1)} + K_p W_r(n) \cdot W_r(n-1) + K_i W_e(n) \] (12)

Where, \( W_e \) = Rotor mechanical speed.

| Table-2 VSI switching sequence based on the Hall Effect sensor signals |
|-------------------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|----------------|
| Ha | Hb | Hc | Ea | Eb | Ec | S1 | S2 | S3 | S4 |
| 0  | 0  | 0  | 0  | 0  | 0  | 0  | 0  | 0  | 0  |
| 0  | 0  | 1  | 0  | 1  | 1  | 0  | 0  | 0  | 0  |
| 0  | 1  | 0  | -1 | -1 | 0  | 1  | 0  | 0  | 0  |
| 0  | 1  | -1 | 0  | 1  | 0  | 1  | 0  | 0  | 0  |
| 1  | 0  | 0  | 1  | -1 | 1  | 0  | 0  | 0  | 0  |
| 1  | 0  | 1  | -1 | 0  | 1  | 0  | 0  | 1  | 0  |
| 1  | 1  | 0  | 0  | -1 | 0  | 0  | 1  | 0  | 0  |
| 1  | 1  | 0  | 0  | 0  | 0  | 0  | 0  | 0  | 0  |

3. Results and Discussion

3.1. Interleaved Boost Converter with 6-switch and 4-switch three phase VSI fed speed control of PI and Fuzzy logical control PMBLDC motor drive

Fig 11(a): Simulation diagram of with PI controller
3.2. No load condition

Fig 12(b): Simulation diagram for speed control of interleaved boost converter based 6- switch three phase VSI fed PMBLDC Motor employing PI and Fuzzy control

Fig 13(a): Speed control of PI

Fig 13(b): Speed of Fuzzy Logical Control

Fig 13: Performance characteristics of Interleaved boost converter with 6- switch VSI fed PMBLDC Motor are shown for a speed of 1000 RPM at No load (TL = 0 N-m)
The PMBLDC motor with 6-switch VSI inverter at no load (TL=0N-m) with a speed of 1000Rpm is considered. Fig13(a): shows the speed, back EMF, stator current and load torque is observed that actual speed of the motor is same as reference speed after the speed is pick up initially. The back EMF is 85 V. Which is trapezoidal in nature. At it is no load condition, starting current of BLDC motor is 8 Amps, then the current falls to zero as observed from current wave form. Fig13(b): shows the performance characteristics of PMBLDC motor topology with 1000Rpm at no load with fuzzy logic controller. The fuzzy logic controller follow the rule base as given in Table-2. The speed is fine turned with FLC .The stator current wave form has lower value of negative peak in case of FLC scheme similary the torque wave form shows lower level of negative peak under no load condition.

Fig14: The back EMF stator current and torque initially exhibit some fluctuations but slowly settle down to steady state value. With fuzzy logic controller, the wave shapes of the performance measures are smoothened with no negative peaks or variations. In the whole in case of No load condition, fuzzy logical control of 4-switch VSI fed PMBLDC motor drive exhibits superior performance compared to its counterparts.

3.3. Application of Load

![Graph 1](image1.png)

![Graph 2](image2.png)

![Graph 3](image3.png)

![Graph 4](image4.png)

Fig14(a): Speed control of PI

Fig14(b): Speed of Fuzzy Logical Control

Fig15: Performance characteristics of Interleaved boost converter with 6-switch VSI fed PMBLDC Motor are shown for a speed of 1000 RPM under loaded condition at 0.3secs (TL = 0.5 N-m).

Fig15(a): describes the performance characteristics of 6 switch VSI fed drive under application of load TL=0.5N-m at time instant 0.3 sec which at time instant 0.3 sec with speed 1000Rpm employing PI control the speed waveform derivates from reference value during application of load at 0.3 sec and restores to normal speed. The back EMF waveform is trapezoidal in nature and is 85V. The starting current is 9Amps. While it is 4.5Amps during loaded condition. The no load torque is 10Nm; while it is
5Nm during loaded condition. Fig 15 (b): Shows PMBLDC drives topology employing fuzzy control. It is observed that’s the speed control is very smooth unlike PI control. Where the actual speed is nearly same a reference speed even after application of load at 0.3 Sec. The current and torque wave forms becomes smooth ere in case of fuzzy control.

3.4. Dynamic response at No load condition

Dynamic performance of BLDC motor with speed variation from 1000Rpm at 0.3 sec is investigated in Fig17(a): and Fig17(b): respectively under no load condition the former uses PI control, while the latter employs fuzzy control. Under loaded condition, the actual speed gradually reaches to the reference speed during the dynamic performance. The back EMF waveform demonstrate variation, with 85V during no load, where as 45V in case of dynamic response. Similarly the stator current and torque fluctuate during dynamic response exhibiting considered negative peaks employing PI control. The fuzzy control variation in the performance characteristics smooth.

Fig16: Performance characteristics of Interleaved boost converter with 4-switch VSI fed PMBLDC Motor are shown for a speed of 1000 RPM under load condition at 0.3secs (TL = 0.5 N-m)

With PI control in case of 4-witch cases the fluctuations are maximum at starting and gradually reduced with PI control. With the fuzzy control, the smooth control is observed with less deviations on negative side. Summarizing the above, we have for laded condition,4-switch VSI fed PMBLDC motor employing Fuzzy control exhibits better performance compared to reset of the configuration.

Fig17: Performance characteristics of Interleaved boost converter with 6-switch VSI fed PMBLDC Motor are shown for a speed change of 1000 RPM to 500 RPM at 0.3Sec at No load (TL = 0 N-m).
3.5. Dynamic response Application of Load

Fig 18(a): Speed control of PI

Fig 18(b): Speed of Fuzzy Logical Control

Fig 18: Performance characteristics of Interleaved boost converter with 4-switch VSI fed PMBLDC Motor are shown for a speed change of 1000 RPM to 500 RPM at 0.3Sec at No load (TL = 0 N-m).

Fig 19(a): Speed control of PI

Fig 19(b): Speed of Fuzzy Logical Control

Fig 19: Performance characteristics of Interleaved boost converter with 6-switch VSI fed PMBLDC Motor are shown for a speed change of 1000 RPM to 500 RPM at 0.3Sec under loaded condition (TL = 0.5 N-m).

Fig 19: In case of with 6-switch configuration PI control during dynamic performance under loaded condition, the fluctuation in stator current and torque are maximum among all configuration. with Fuzzy control, the deviations are reduced. In case of 4-switch configuration with PI control the fluctuation in the performance measures are lesser.
4. Conclusion

Soft switching of interleaved boost converter with 4 switch configuration and 6 switch configuration based VSI fed PMBLDC motor was developed in this paper. The performance evaluation of the above mentioned topologies are studied and analyzed under different operating conditions. The results, shows that 4-switch configuration with Fuzzy control is proved to be more effective than 6-switch converter based drive in term of fine control and tuning, minimizing of fluctuation in stator current and output torque ripple etc.

References


