FAULT ANALYSIS OF FIVE PHASE TRANSMISSION SYSTEM

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Abstract: Transmission infrastructure is congested due to a combination of increasing load demands, declining investment, and aging facilities. It is anticipated that significant investments will be required for new construction and upgrades in order to serve load demands. High Phase order transmission system is being well thought-out a possible alternative for increasing the power transmission capacity of overhead electric power transmission. High phase order system will experienced quickly with external or internal faults. When fault current magnitudes are important, it can cause smash up to equipment. It is important to design the power system such as the fault is out-of-the-way quickly to minimize the equipment damage and improve own safety. Fault analysis is the stage to determine the magnitude of the current flowing throughout the power system at assorted time intervals after a fault. In this paper, the different types of fault analysis techniques used to analyze the level of fault in Five Phase Transmission System. This Analysis is used to identify the Level of Protection. MATLAB Simulation results give the wave shape of the fault current.

Keywords: Fault analysis, Symmetrical Components, Sequence impedance of Transmission line.

1. Introduction

In INDIA, some states and regions have high demand load growth, due to the growth of the economy and the population. The high load demand growth requires more power transfer capability of the existing transmission infrastructure. Considering the lengthy process of transmission line construction, investment on transmission grid should be made for the long term. Additionally, the areas with high level load demands are suffering critical transmission grid congestion, because of the limited capacity of transmission infrastructure. The congestion areas also demand additional transmission capability to deliver more power from neighboring areas. Meanwhile, most of the existing transmission infrastructures, which were constructed in 1950’s, are aging. According to the data from Department of Energy (DoE), 70% of transmission lines and transformers are more-than 25 years old and 60% of circuit breakers are more than 30 years old[1]. To meet the challenges on the transmission grid and improve power transfer capability, billions of dollars will be spent on new transmission infrastructure construction and upgrades.

The most common approach to increase power transfer capability is increasing system voltage. System voltage has been steadily increased from 115kV to 1000kV for AC transmission lines and ±400kV to ±800kV DC lines have been either built or under construction worldwide [2]. However, in the INDIA, constructing or upgrading a transmission line today is more complicated, compared to decades ago. Some factors responsible for this are listed below:

a. Social concerns about the impact of transmission lines: Much more attention is paid to constructing and upgrading transmission lines today [3]. Electric and Magnetic Fields (EMF) and impacts of transmission lines on daily life have provoked intense criticism [4].

b. Laws and standards issued by governments and organizations: In many states, the maximum values of transmission line electric and magnetic fields are regulated. Organizations in power industry, such as Institute of Electrical and Electronics Engineers (IEEE), and Electric Power Research Institute (EPRI), also published standards to define acceptable transmission electric and magnetic fields criteria. The difficulty of constructing or upgrading higher voltage level transmission lines with limited Right of Way (RoW) increases because of those criteria.

c. Charge of the transmission line corridor: The cost of obtaining the transmission line corridor is expensive in those metropolitan areas and regions. Some residential communities have been built around existing transmission lines and difficult to extend the Right of Way (RoW)[3]. Line compaction, Higher Phase Order (HPO) systems, and High Temperature Low Sag (HTLS) conductors, can be used to boost the power convey capability of transmission lines with limited Right of Way (RoW). Additional phases increases the number of possible faults highly and consequently complicate. For instance, the no.of faults in a five phase system be 61 where as it is only 11 in a three phase system. Fault occur one time in a while due to lighting, flashover due to contaminated insulation, diminishing of tree branches on the overhead system, animal invasion and flawed operations. When the fault current magnitudes are momentous, it can cause harm to equipment and
detonation if the fault is not cleared for protracted time. Also, electrical fire hazards to people are probable in a faulted power system. Therefore, it is significant to design the power system such as the fault is out-of-the-way quickly to minimize the equipment harm and advance personal safety. Fault analysis is performed to conclude the magnitude of the current flowing all the way through the power system at an assortment of time intervals after a fault. The magnitude of the current all the way through the power system after a fault varies with time awaiting it reaches a steady state situation. During the fault, the power system is called on to sense, suspend and cut off these faults. The duty frightened on the equipment is dependent on the magnitude of the current, which is a job of the time of fault instigation. The calculated fault currents are used to pick fuses, circuit breakers and surge protective relays. The balanced component model and phase coordinate method is used in the analysis of the asymmetrical faults.

Applying Five Phase alternative in Transmission planning is the design of an ample protective scheme. This requires an in depth and practical fault analysis which is the main argument of this paper. General definition for fault is “Any failure that interferes with the normal flow of current”. The fault current that occurs in power system is regularly huge and precarious. Faults can occur because of short circuit or partial short circuit, open circuit, unequal conditions or any other reasons. Short circuit faults are the majority and most harmful, as the excessive current can source thermal and mechanical damage to the plant shipping it.

The complexity of fault examination in a five-phase system is bigger than in a three-phase system. Five-phase faults are the least common while single-line to ground faults is the most common. In addition, faults involving two and three phases with several dissimilar possibilities could be more frequent in five-phase systems compared to three-phase system.

### 2. Faults on Five – Phase Power System

The faults in Five Phase System are of Symmetrical or Unsymmetrical types, which may consist of short circuits, faults through impedances or open conductors. The fault investigation in FivePhase System is more convoluted than in the usual three phase systems. This is due to the fact that there are five phases, each subjected to a different voltage, and the existence of a neutral makes the number of fault types much larger.

#### TABLE I

<table>
<thead>
<tr>
<th>Fault Type</th>
<th>No. of Combinations</th>
<th>Significant Combinations</th>
<th>Example</th>
</tr>
</thead>
<tbody>
<tr>
<td>5 Phase</td>
<td>1</td>
<td>1</td>
<td>a-b-c-d-e</td>
</tr>
<tr>
<td>5 Phase to Neutral</td>
<td>1</td>
<td>1</td>
<td>a-b-c-d-e-n</td>
</tr>
</tbody>
</table>

The number of likely significant faults in three phase and five phase systems is 5 and 13 respectively. The 15 two phase fault combinations are reducing to two major combinations are: Fault between phases 72° apart (e.g.: phase a to phase b), Fault between phases 144° apart (e.g.: phase a to phase c). In a similar manner, analysis for all faults can be restricted to 13 significant combinations as mentioned.

#### 3. Sequence Impedance of Transmission Line

In classify to analyze uneven conditions on transmission lines, need to concern the method of symmetrical components. To do so, first need to convey the impedance of a transmission line as positive-, negative- and zero-sequence mechanism. Fig. 1 shows the circuit of a fully transposed line carrying unbalanced currents. The return path for Ia is sufficiently away for the mutual effect to be ignored. Let Zs = Each Line Self Impedance, Zm = Pair of lines Mutual Impedance.

The following KVL equations can be written from Fig. 1.

$$V_a - V_{1a} = Z_a I_a + Z_m I_b + Z_m I_c$$

$$V_b - V_{1b} = Z_m I_a + Z_b I_b + Z_m I_c$$

$$V_c - V_{1c} = Z_m I_a + Z_m I_b + Z_c I_c$$

In matrix form:

$$\begin{bmatrix} V_a - V_{1a} \\ V_b - V_{1b} \\ V_c - V_{1c} \end{bmatrix} = \begin{bmatrix} Z_a & Z_m & Z_m \\ Z_m & Z_b & Z_m \\ Z_m & Z_m & Z_c \end{bmatrix} \begin{bmatrix} I_a \\ I_b \\ I_c \end{bmatrix}$$ (1)

The above equation can be written as:

$$\Delta V = \Delta S I$$ (3)

Where, $\Delta S$ = Transformation Matrix =

$$\begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & a^1 & a^2 & a \\ 1 & a & a^2 & a^3 \\ 1 & a^2 & a^3 & a^1 \end{bmatrix}$$
4. Symmetrical Components

The method of symmetrical mechanism is the most authoritative tool for dealing with unbalanced poly phase systems. In 1918, C. L. Fortescue projected a system whereby a set of n – unbalanced phasors may be resolved into (n-1) balanced, n – phase systems of dissimilar phase sequence and one zero phase sequence system. According to Fortescue’s theorem, five unbalance (voltage or current) phasors of a five – phase system be able to be resolved into five balanced systems of phasors (see Fig. 2).

The balanced sets of components are:

1\textsuperscript{st} (or +ve) sequence components
2\textsuperscript{nd} sequence components
3\textsuperscript{rd} sequence components
4\textsuperscript{th} (or -ve) sequence components

Zero sequence components

Each of the i\textsuperscript{th} sequence components (i = 0, 1, 2, 3, 4) consists of five phasors like in magnitude and displaced with other by (72\textdegree i)\textsuperscript{o} in phase except the zero sequence component, which is co-phasal (i.e. all in the same phase). Let the five phases of original system be a, b, c, d and e with phase sequence “abcde”. For 2nd sequence the phase sequence is “adbec”, for 3rd sequence the phase sequence is “acebd”, for 4th sequence the phase sequence is “aeceb”. A Five Phase system of unbalanced voltage phasors can be uttered in terms of their balanced components by the equations shown below:

\[
Z = \begin{bmatrix}
Z_0 & Z_a & Z_b & Z_c & Z_d & Z_e
\end{bmatrix}
\begin{bmatrix}
1 & 1 & 1 & 1 & 1
a^0 & a & a^2 & a^3 & a^4
a^1 & a^2 & a^3 & a^4 & a^0
a^2 & a^3 & a^4 & a^0 & a^1
a^3 & a^4 & a^0 & a^1 & a^2
\end{bmatrix}
\begin{bmatrix}
1
a
a^2
a^3
a^4
\end{bmatrix}
\]

\[
A_0^2 Z_a = \begin{bmatrix}
0 & 0 & 0 & 0 & 0 & 0
0 & 0 & Z_r - Z_a & 0 & 0 & 0
0 & 0 & 0 & Z_r - Z_a & 0 & 0
0 & 0 & 0 & 0 & Z_r - Z_a & 0
0 & 0 & 0 & 0 & 0 & Z_r - Z_a
\end{bmatrix}
\]

Therefore,

\[
\begin{bmatrix}
0 - V_{a0}
0 - V_{b0}
0 - V_{c0}
0 - V_{d0}
0 - V_{e0}
\end{bmatrix} = \begin{bmatrix}
Z_0 + 4Z_a
0 & 0 & 0 & 0 & 0
0 & 0 & Z_r - Z_a & 0 & 0
0 & 0 & 0 & Z_r - Z_a & 0
0 & 0 & 0 & 0 & Z_r - Z_a
0 & 0 & 0 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
I_{a0}
I_{b0}
I_{c0}
I_{d0}
I_{e0}
\end{bmatrix}
\]

\[
I_{a0} = I_a + I_b + I_c + I_d + I_e
\]

Fig. 1. Circuit of Fully Transposed Line.

Where,

\[Z_0 = Z_0 + 4Z_a = \text{Impedance of Zero sequence}\]
\[Z_a = Z_r - Z_a = \text{Impedance of First (or +ve) sequence}\]
\[Z_b = Z_r - Z_a = \text{Impedance of Second sequence}\]
\[Z_c = Z_r - Z_a = \text{Impedance of Third sequence}\]
\[Z_d = Z_r - Z_a = \text{Impedance of Fourth (or -ve) sequence}\]

\[
V_{a1} = aV_{a1} \quad V_{b1} = a^2V_{a1} \quad V_{c2} = aV_{a1} \quad V_{d2} = a^3V_{a1} \quad V_{e4} = a^4V_{a1}
\]

\[
\begin{align*}
V_{a1} &= V_{a1} + V_{a2} + V_{a3} + V_{a4} + V_{a0} \\
V_{b1} &= a^1V_{a1} + a^2V_{a2} + a^3V_{a3} + a^4V_{a4} + V_{a0} \\
V_{c2} &= a^3V_{a1} + a^1V_{a2} + a^2V_{a3} + a^4V_{a4} + V_{a0} \\
V_{d2} &= a^1V_{a1} + a^2V_{a2} + a^3V_{a3} + a^4V_{a4} + V_{a0} \\
V_{e4} &= a^4V_{a1} + a^1V_{a2} + a^2V_{a3} + a^3V_{a4} + V_{a0}
\end{align*}
\]

in matrix form:

\[
\begin{bmatrix}
V_a \\
V_b \\
V_c \\
V_d \\
V_e
\end{bmatrix} = \begin{bmatrix}
1 & 1 & 1 & 1 & 1 \\
1 & a^4 & a^3 & a^2 & a \\
1 & a^3 & a^4 & a^2 & a \\
1 & a^2 & a^4 & a^3 & a \\
1 & a^3 & a^2 & a^4 & a
\end{bmatrix} \begin{bmatrix}
V_{a0} \\
V_{b0} \\
V_{c0} \\
V_{d0} \\
V_{e0}
\end{bmatrix}
\]

also be written as:

\[
\begin{bmatrix}
V_{a0} \\
V_{b0} \\
V_{c0} \\
V_{d0} \\
V_{e0}
\end{bmatrix} = \frac{1}{5} \begin{bmatrix}
1 & 1 & 1 & 1 & 1 \\
1 & a & a^2 & a^3 & a^4 \\
1 & a^2 & a & a^3 & a^4 \\
1 & a^3 & a^2 & a & a^4 \\
1 & a^4 & a^3 & a^2 & a
\end{bmatrix} \begin{bmatrix}
V_a \\
V_b \\
V_c \\
V_d \\
V_e
\end{bmatrix}
\]
The five phase operator, $a = 1/\sqrt{72} = 0.309 + j0.951$
$a^2 = 1/\sqrt{144}$
$a^3 = 1/\sqrt{-72}$
$1 + a + a^2 + a^3 + a^4 = 0$

5. Fault Analysis

I. Using Symmetrical Components method:

a. Single Line to Ground Fault (SLG) (a-n)

Boundary conditions: $V_a = 0$ and $I_a = I_b = I_c = I_d = 0$

Substituting above equations in below equation:

$$
\begin{bmatrix}
I_a & I_b & I_c & I_d & I_e \\
1 & 1 & 1 & 1 & 1 & I_a \\
1 & a & a^2 & a^3 & a^4 & I_b \\
1 & a^2 & a^3 & a^4 & a & I_c \\
1 & a^3 & a^4 & a & a^2 & I_d \\
1 & a^4 & a & a^2 & a^3 & I_e \\
\end{bmatrix}
= 1/5
\begin{bmatrix}
I_a \\
I_b \\
I_c \\
I_d \\
I_e \\
\end{bmatrix}
$$

So, $I_a = I_1 = I_2 = I_3 = I_4 = I_5 = 1/5$

From equation (4)

$$
\begin{bmatrix}
v_a \\
v_b \\
v_c \\
v_d \\
v_e \\
\end{bmatrix}
= \begin{bmatrix}
0 \\
z_o & 0 & 0 & 0 & 0 \\
0 & 0 & z_i & 0 & 0 \\
0 & 0 & 0 & z_e & 0 \\
0 & 0 & 0 & 0 & z_o \\
0 & 0 & 0 & 0 & z_i \\
\end{bmatrix}
\begin{bmatrix}
I_a \\
I_b \\
I_c \\
I_d \\
I_e \\
\end{bmatrix}
$$

From boundary conditions:

$$v_a = v_o + v_{at} + v_{a2} + v_{a3} + v_{a4} = 0$$

Solving above equation:

$$I_1 = E_0/(Z_1 + Z_2 + Z_3 + Z_4)$$

Fault Current, $I_f = 5v/(E/Z_0 + Z_1 + Z_2 + Z_3 + Z_4)$

b. Four Line to Ground Fault (4LG) (b-c-d-e-n)

Boundary conditions: $V_b = V_c = V_d = V_e = 0$

and $I_a = I_b = I_c = I_d = I_e = I_5 = 1/5$

$$\begin{bmatrix}
v_a \\
v_b \\
v_c \\
v_d \\
v_e \\
\end{bmatrix}
= \begin{bmatrix}
1 & 1 & 1 & 1 & 1 & v_a \\
1 & a & a^2 & a^3 & a^4 & v_b \\
1 & a^2 & a^3 & a^4 & a & v_c \\
1 & a^3 & a^4 & a & a^2 & v_d \\
1 & a^4 & a & a^2 & a^3 & v_e \\
\end{bmatrix}
= 1/5
\begin{bmatrix}
v_a \\
v_b \\
v_c \\
v_d \\
v_e \\
\end{bmatrix}
$$

By substituting periphery conditions in above equation we get:

$v_o = v_j/5$:

$$v_{at} = v_{a2} = v_{a3} = v_{a4} = 1/5(v_o + a^4v_b + a^3v_c + a^2v_d + av_e) = v_o/5.$$

From equation (1) we get:

$$\begin{bmatrix}
Z_0 & 0 & 0 & 0 & 0 \\
0 & Z_1 & 0 & 0 & 0 \\
0 & 0 & Z_2 & 0 & 0 \\
0 & 0 & 0 & Z_3 & 0 \\
0 & 0 & 0 & 0 & Z_4 \\
\end{bmatrix}
= \begin{bmatrix}
I_{at} \\
I_{a2} \\
I_{a3} \\
I_{a4} \\
I_{a5} \\
\end{bmatrix}
= \begin{bmatrix}
0 \\
E_a - I_{a1}Z_1 \\
E_a - I_{a1}Z_2 \\
E_a - I_{a1}Z_3 \\
E_a - I_{a1}Z_4 \\
\end{bmatrix}
$$

Now,

$$I_0 + I_1 + I_2 + I_3 + I_4 = (E/Z_0) + (I_{a1}Z_0/Z_0) + I_{a1} - (E/Z_2) + (I_{a1}Z_2/Z_2) - (E/Z_3) + (I_{a1}Z_3/Z_3) - (E/Z_4) + (I_{a1}Z_4/Z_4)$$

From boundary conditions:

$$I_{a1} = 1 + Z_1 \begin{bmatrix} 1 & 1 & 1 & 1 & 1 \end{bmatrix} E_a \begin{bmatrix} 1 & 1 & 1 & 1 \end{bmatrix} Z_1 \begin{bmatrix} 1 & 1 & 1 & 1 \end{bmatrix} Z_1$$

$$I_{a1} = \frac{Z_2Z_3Z_4}{Z_2Z_3Z_4 + Z_0Z_2 + Z_0Z_3 + Z_0Z_4 + Z_0Z_1} = E_a$$

$$I_{a1} = \frac{E_a}{Z_2Z_3Z_4}$$

(21)

Now by using equation (16) we can find fault current for 4LG fault. Although the six phase symmetrical component method seems to be a accurate technique for calculating faults in a six phase system, it has many drawbacks, first this method has been for just some simple faults in isolated five phase system and all important faults that could take place on five phase line are not willing to symmetrical component analysis. Second, the fault resistance has been ignored, which with existence of fault resistance the calculation become more complicated and the third and main drawback of this method is that in case of simultaneous faults on the system symmetrical component becomes rather complicated to use.

II. Fault Analysis using Phase Coordinates method:

The phase coordinate method is usually used in three phase for analyzing faults in an unbalanced system like distribution and also it is a useful tool for analyzing simultaneous faults, because in these situations the symmetrical component does not provide any advantages to ease the problem at hand. Equation (2) is used to conduct fault analysis in this method.

a. Single Line to Ground Fault (SLG) (a-n)

Boundary conditions: $V_a = 0$ and $I_a = I_b = I_c = I_d = 0$

by substituting these conditions in equation (2) we get:

$$\begin{bmatrix}
v_x - v_e \\
a^2v_x - v_e \\
a^3v_x - v_e \\
a^4v_x - v_e \\
a^5v_x - v_e \\
\end{bmatrix}
= \begin{bmatrix}
Z_x & Z_x & Z_x & Z_x & Z_x \\
Z_x & Z_x & Z_x & Z_x & Z_x \\
Z_x & Z_x & Z_x & Z_x & Z_x \\
Z_x & Z_x & Z_x & Z_x & Z_x \\
Z_x & Z_x & Z_x & Z_x & Z_x \\
\end{bmatrix}
\begin{bmatrix}
I_x \\
I_x \\
I_x \\
I_x \\
I_x \\
\end{bmatrix}
$$

From above equation we get:: $I_a = v_x/Z_x$.

b. Double Line to Ground Fault (LLG) (a-b-n)

Boundary conditions: $V_a = V_b = 0$ and $I_a = I_b = I_c = 0$

by substituting these conditions in equation (4) we get:

$$\begin{bmatrix}
v_x - v_e \\
a^2v_x - v_e \\
a^3v_x - v_e \\
a^4v_x - v_e \\
a^5v_x - v_e \\
\end{bmatrix}
= \begin{bmatrix}
Z_x & Z_x & Z_x & Z_x & Z_x \\
Z_x & Z_x & Z_x & Z_x & Z_x \\
Z_x & Z_x & Z_x & Z_x & Z_x \\
Z_x & Z_x & Z_x & Z_x & Z_x \\
Z_x & Z_x & Z_x & Z_x & Z_x \\
\end{bmatrix}
\begin{bmatrix}
I_x \\
I_x \\
I_x \\
I_x \\
I_x \\
\end{bmatrix}
$$

By solving above equation we get:

$$a^4v_x = Z_0I_b + Z_1I_b$$

(24)

$$I_b = (a^4v_x - Z_0I_a)/Z_1$$

(25)

$$a^4v_x = Z_0I_b + Z_1I_b$$

By substituting equation (24) in equation (25) we get:

$$I_a = (E_x(a^4Z_x - a^4Z_b))/(Z_x + Z_1)(Z_1 - Z_2)$$

(26)
c. Triple Line to Ground Fault (3LG) (a-b-c-n)

Boundary conditions: \( V_a = V_b = V_c = 0 \) and \( I_d = I_e = 0 \), by substituting these conditions in equation (2) we get:

\[
\begin{bmatrix}
\alpha^2v_a - 0 \\
\alpha^2v_b - 0 \\
\alpha^2v_c - 0 \\
\alpha v_d - v_e \\
\end{bmatrix}
= \begin{bmatrix}
Z_a & Z_a & Z_a & Z_a & Z_a \\
Z_a & Z_b & Z_b & Z_b & Z_b \\
Z_b & Z_b & Z_b & Z_b & Z_b \\
Z_a & Z_b & Z_b & Z_b & Z_b \\
\end{bmatrix}
\begin{bmatrix}
I_a \\
I_b \\
I_c \\
I_d \\
\end{bmatrix}
\]

From the above equation:

\[ V_a = Z_a I_a + Z_b I_b + Z_c I_c \]
\[ \alpha v_a = Z_b I_a + Z_c I_b + Z_a I_c \]
\[ \alpha v_b = Z_a I_a + Z_b I_b + Z_c I_c \]
\[ \alpha v_c = Z_a I_a + Z_b I_b + Z_c I_c \]
\[ \alpha v_d = Z_a I_a + Z_b I_b + Z_c I_c \]
\[ \alpha v_e = Z_a I_a + Z_b I_b + Z_c I_c \]

From the above equation:

\[ V_a = Z_a I_a + Z_b I_b + Z_c I_c \]
\[ \alpha v_a = Z_b I_a + Z_c I_b + Z_a I_c \]
\[ \alpha v_b = Z_a I_a + Z_b I_b + Z_c I_c \]
\[ \alpha v_c = Z_a I_a + Z_b I_b + Z_c I_c \]
\[ \alpha v_d = Z_a I_a + Z_b I_b + Z_c I_c \]
\[ \alpha v_e = Z_a I_a + Z_b I_b + Z_c I_c \]

From the above equations we can solve fault current for 3LG fault.

d. Four Line to Ground Fault (4LG) (a-b-c-d-n)

Boundary conditions: \( V_a = V_b = V_c = V_d = 0 \) and \( I_e = 0 \), by substituting these conditions in equation (2) we get:

\[
\begin{bmatrix}
\alpha^2v_a - 0 \\
\alpha^2v_b - 0 \\
\alpha^2v_c - 0 \\
\alpha^2v_d - 0 \\
\alpha v_e - v_f \\
\end{bmatrix}
= \begin{bmatrix}
Z_a & Z_a & Z_a & Z_a & Z_a \\
Z_a & Z_b & Z_b & Z_b & Z_b \\
Z_b & Z_b & Z_b & Z_b & Z_b \\
Z_a & Z_b & Z_b & Z_b & Z_b \\
Z_a & Z_a & Z_a & Z_a & Z_a \\
\end{bmatrix}
\begin{bmatrix}
I_a \\
I_b \\
I_c \\
I_d \\
I_e \\
\end{bmatrix}
\]

From the above equation:

\[ V_a = Z_a I_a + Z_b I_b + Z_c I_c + Z_d I_d \]
\[ \alpha v_a = Z_b I_a + Z_c I_b + Z_d I_c + Z_d I_d \]
\[ \alpha v_b = Z_a I_a + Z_b I_b + Z_c I_c + Z_d I_d \]
\[ \alpha v_c = Z_a I_a + Z_b I_b + Z_c I_c + Z_d I_d \]
\[ \alpha v_d = Z_a I_a + Z_b I_b + Z_c I_c + Z_d I_d \]
\[ \alpha v_e = Z_a I_a + Z_a I_b + Z_a I_c + Z_a I_d \]

From the above equations we can solve fault current for 4LG fault.

e. Five Line to Ground Fault (5LG) (a-b-c-d-e-n)

Boundary conditions: \( V_a = V_b = V_c = V_d = V_e = 0 \), by substituting these conditions in equation (2) we get:

\[
\begin{bmatrix}
\alpha^2v_a - 0 \\
\alpha^2v_b - 0 \\
\alpha^2v_c - 0 \\
\alpha^2v_d - 0 \\
\alpha^2v_e - 0 \\
\end{bmatrix}
= \begin{bmatrix}
Z_a & Z_a & Z_a & Z_a & Z_a \\
Z_a & Z_b & Z_b & Z_b & Z_b \\
Z_b & Z_b & Z_b & Z_b & Z_b \\
Z_a & Z_b & Z_b & Z_b & Z_b \\
Z_a & Z_a & Z_a & Z_a & Z_a \\
\end{bmatrix}
\begin{bmatrix}
I_a \\
I_b \\
I_c \\
I_d \\
I_e \\
\end{bmatrix}
\]

From the above equation:

\[ V_a = Z_a I_a + Z_b I_b + Z_c I_c + Z_d I_d + Z_e I_e \]
\[ \alpha v_a = Z_b I_a + Z_c I_b + Z_d I_c + Z_d I_d + Z_e I_e \]
\[ \alpha v_b = Z_a I_a + Z_b I_b + Z_c I_c + Z_d I_d + Z_e I_e \]
\[ \alpha v_c = Z_a I_a + Z_b I_b + Z_c I_c + Z_d I_d + Z_e I_e \]
\[ \alpha v_d = Z_a I_a + Z_b I_b + Z_c I_c + Z_d I_d + Z_e I_e \]
\[ \alpha v_e = Z_a I_a + Z_a I_b + Z_a I_c + Z_a I_d + Z_a I_e \]

From the above equations we can solve fault current for 5LG fault.

6. Fault Analysis using MATLAB Simulink

Conventional three phase supply is converted into five phase supply by using specially connected transformer connection. Later the five phase power is transmitted by using PI section lines as shown in Fig. 3. The input parameters used in simulation are shown in Table II.
Fig. 5. DLG (b – d – n) Fault Currents
Fig. 6. DLG (a – b – n) Fault Currents
Fig. 7. TLG (a – c – d – e – n) Fault Currents
Fig. 8. TLG (a – b – c – n) Fault Currents
For the healthy system, introducing Triple Line to Ground (TLG) Fault applied at 0.05sec to 0.15sec by operating switch in MATLAB Simulation to Phase – A, Phase – C & Phase – E (a-c-e-n) as shown in Fig. 7 and Phase – A, Phase – B & Phase – C (a-b-c-n) as shown in Fig. 8, observe the abnormal raise the current in three phases.

For the healthy system, introducing Double Line to Ground (DLG) Fault applied at 0.05sec to 0.15sec by operating switch in MATLAB Simulation to Phase – B & Phase – D (b-d-n) as shown in Fig. 5 and Phase – A & Phase – B (a-b-n) as shown in Fig. 6, observe the abnormal raise the current at both the phases.

For the healthy system, introducing Quadratic Line to Ground (QLG) Fault applied at 0.05sec to 0.15sec by operating switch in MATLAB Simulation to Phase – B, Phase – C, Phase – D & Phase – E (b-c-d-e-n). From the Fig. 9, observe the abnormal raise the current in four phases.

For the healthy system, introducing Pentad Line to
Ground (PLG) Fault applied at 0.05sec to 0.15sec by operating switch in MATLAB Simulation to Phase – A, Phase – B, Phase – C, Phase - D & Phase – E (a-b-c-d-e-n). Form all the waveforms, observe the abnormal raise the current in the respective phases. The steady state current value is 3720A at fault condition.

**TABLE III**

Current values at fault condition from MATLAB Simulink

<table>
<thead>
<tr>
<th>7 significant ground faults</th>
<th>Sub-Transient current</th>
<th>Transient current</th>
<th>Steady State Current</th>
</tr>
</thead>
<tbody>
<tr>
<td>SLG (d-n)</td>
<td>4400A</td>
<td>3720A</td>
<td>395A</td>
</tr>
<tr>
<td>DLG (b-d-n)</td>
<td>b – 3750A d – 4400A</td>
<td>3720A</td>
<td>395A</td>
</tr>
<tr>
<td>DLG (a-b-n)</td>
<td>a – 3540A b – 3725A</td>
<td>3720A</td>
<td>395A</td>
</tr>
<tr>
<td>TLG (a-c-e-n)</td>
<td>a – 3550A c – 4400A e – 2200A</td>
<td>3720A</td>
<td>395A</td>
</tr>
<tr>
<td>TLG (a-b-c-n)</td>
<td>a – 3548A b – 3750A c – 4350A</td>
<td>3720A</td>
<td>395A</td>
</tr>
<tr>
<td>QLG (b-c-d-e-n)</td>
<td>b – 3725A c – 4400A d – 2250A</td>
<td>3720A</td>
<td>395A</td>
</tr>
<tr>
<td>PLG (a-b-c-d-e-n)</td>
<td>a – 3545A b – 3760A c – 4400A d – 2250A</td>
<td>3720A</td>
<td>395A</td>
</tr>
</tbody>
</table>

Similarly, same result obtained from NI Multisim based simulation for all the seven significant faults. NI Multisim simulation diagram shown in fig.11. Some slight differences in current values from MATLAB Simulation to NI Multisim based Simulation. Because, NI Multisim based simulation is somewhat near to the real world.

**TABLE IV**

Current values at fault condition from NI Multisim

<table>
<thead>
<tr>
<th>7-Significant ground faults</th>
<th>Transient current</th>
<th>Steady State Current</th>
</tr>
</thead>
<tbody>
<tr>
<td>SLG (d-n)</td>
<td>3.681kA</td>
<td>394.829A</td>
</tr>
<tr>
<td>DLG (b-d-n)</td>
<td>b – 3.681kA d – 3.681kA</td>
<td>394.829A</td>
</tr>
<tr>
<td>DLG (a-b-n)</td>
<td>a – 3.681kA b – 3.681kA</td>
<td>394.829A</td>
</tr>
<tr>
<td>TLG (a-c-e-n)</td>
<td>a – 3.681kA c – 3.681kA e – 3.681kA</td>
<td>394.829A</td>
</tr>
<tr>
<td>TLG (a-b-c-n)</td>
<td>a – 3.681kA b – 3.681kA c – 3.681kA</td>
<td>394.829A</td>
</tr>
<tr>
<td>QLG (b-c-d-e-n)</td>
<td>b – 3.681kA c – 3.681kA d – 3.681kA e – 3.681kA</td>
<td>394.829A</td>
</tr>
</tbody>
</table>

Fig. 11: NI Multisim based Simulation results for SLG (d-n) fault.
Conclusion

Fault analysis is performed to conclude the magnitude of the current elegant throughout the power system at a range of time intervals after a fault. The symmetrical component method and phase coordinate methods are very useful techniques to analysis the fault in a power system. MATLAB Simulation results and NI Multisim based simulation results are obtained and presented in this paper for different fault conditions.

References